Form 1449 (Modified) **Information Disclosure Statement By Applicant** (Use Several Sheets if Necessary)	Atty Docket No. ALTRP134/1466 Applicant: James Loran Ball Filing Date March 31, 2004	Application No.: 10/815,478 Group 2183
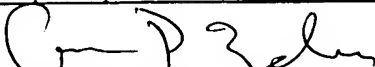
U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	A						
	B						
	C						
	D						
	E						
	F						
	G						

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation Yes	No
	J							
	K							
	L							
	M							
	N							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
G↲	O	Linley Gwennap, "Intel Discloses New IA-64 Features Rotating Registers Reduce Code Expansion: Merced Touted for Big Servers" Microdesign Resources, March 8, 1999, Microprocessor Report
G↲	P	Lars T. Hansen, "Larceny Note #6: Larceny on the SPARC" retrieved from www.ccs.neu.edu/home/will/larceny/notes/notes6-sparc.html, May 5, 1998
G↲	Q	MIPS Instruction Reference, retrieved from www.mrc.uidaho.edu/mrc/people/jff/digital/MIPSir.html. updated September 10, 1998
G↲	R	Nikolova et al., "A Compression/Decompression Scheme for Embedded Systems Code", Electronic Systems and Control Division Research 2003, pp. 36-38.
Examiner		Date Considered 4/26/06

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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